

Serial No. 09/985,766

Docket No.: 1448.1017

REMARKS

In accordance with the foregoing, claims 1, 2, 4, 7, 8, 9 and 11 have been amended to clarify salient features of the invention and both those claims along with the remaining claims have been amended to improve form. No new matter is presented and, accordingly, it is respectfully requested that the amended claims be entered herein.

Claims 1-11 are pending and under consideration herein.

STATUS OF CLAIMS

All of the pending claims 1-11 are rejected.

ITEMS 3 – 21: REJECTION OF CLAIMS 1-11 FOR OBVIOUSNESS UNDER 35 U.S.C. §103(a) OVER BOCK ET AL. (U.S. PATENT 5,155,856), IBM TECHNICAL DISCLOSURE BUTTLETIN ACC-NO NN79013353, AND MACGINITIE ET AL. (U.S. PATENT 4,688,212)

The rejection is respectfully traversed.

In accordance with the foregoing, various of the claims have been amended, based substantially on disclosures at page 19, line 20 through page 21, line 6 and page 22, lines 17-24 of the specification, further taking into account FIGS. 11 and 12.

More particularly, the present invention functions to avoid a problem wherein a processor does not reach a fully activated state. That function may be characterized in a specific embodiment as follows:

1. When the HRST signal (second signal) is input, the second initial setting area is initialized after confirming the value of the PC bit 31 (first flag) is set, regardless of whether or not the PC bit 31 is reset.
2. When the SRST signal (third signal) is input, the third initial setting area is initialized after confirming the values of the PC bits 31 and 32 (first flag and second flag) are set, regardless of whether or not the PC bits 31 and 32 are reset.

It is respectfully submitted that the function as above described, supporting the claimed invention herein, distinguishes patentably over the references taken singularly or in any proper combination.

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CONCLUSION

It is respectfully submitted that the pending claims patentably distinguish over the references of record.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being transmitted via facsimile to: Commissioner for Patents,
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STAAS & HALSEY

By: Date: 1-3-05